

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/643,461	XIANG ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Joseph Nguyen	2815	

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. 1, pages 199 and 647.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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